

FIG 1

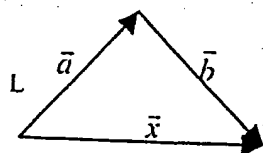


FIG 2A

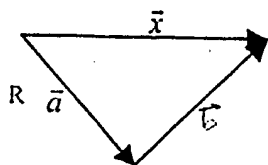


FIG 2B

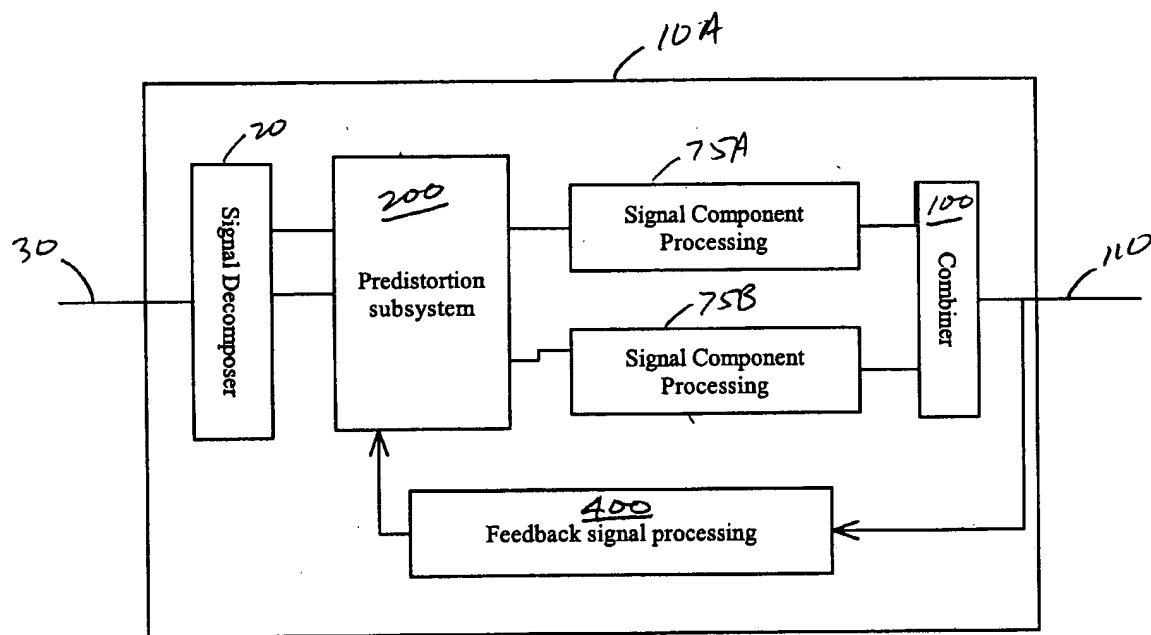


FIG 7

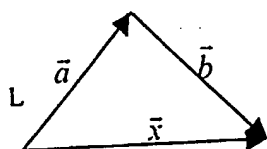


FIG 3A

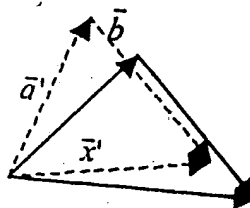


FIG 3C

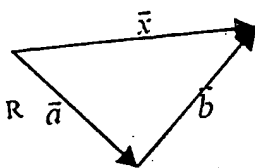


FIG 3B

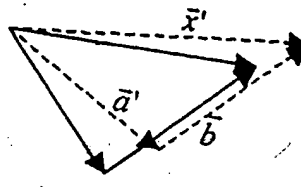


FIG 3D

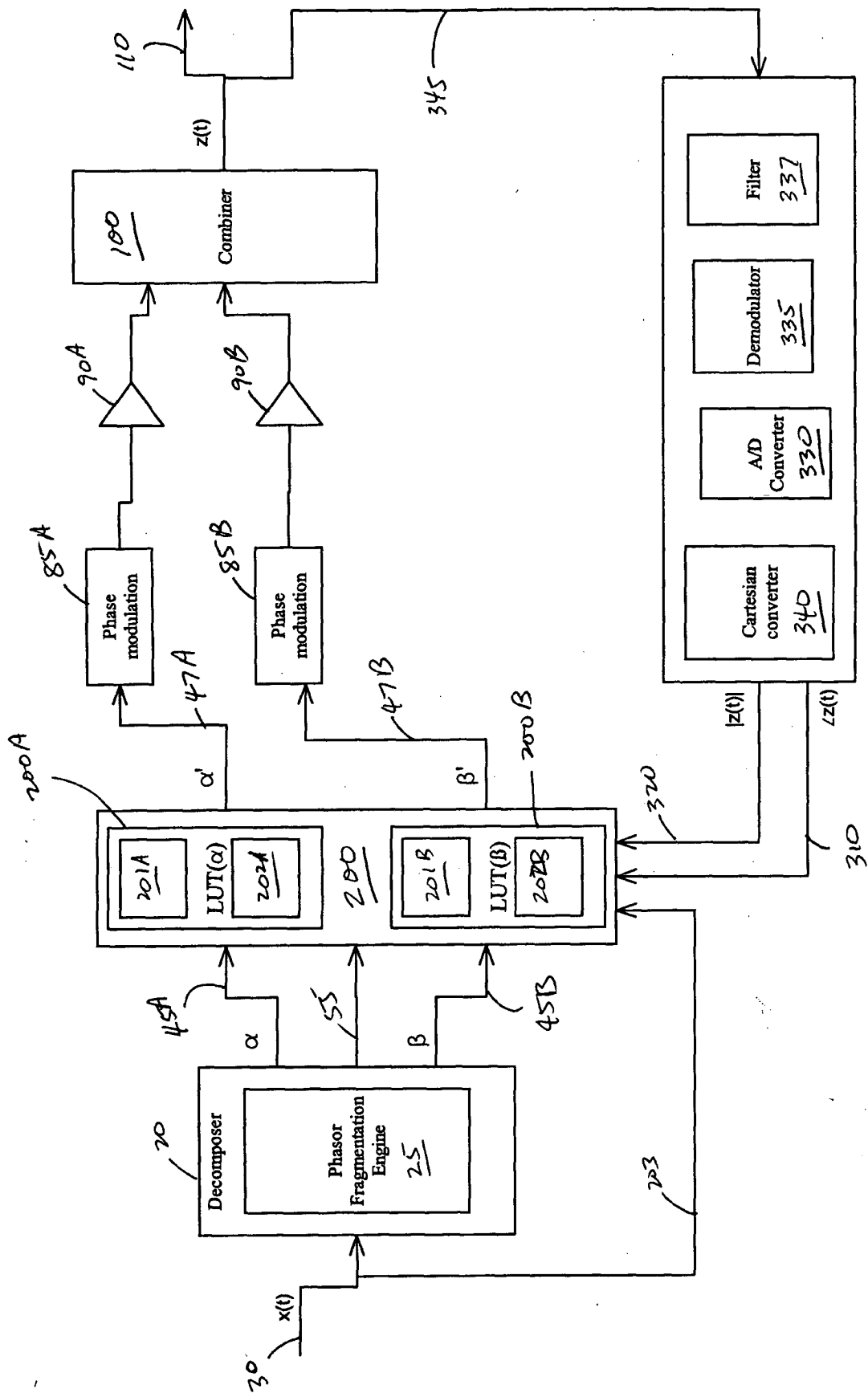


FIG 4

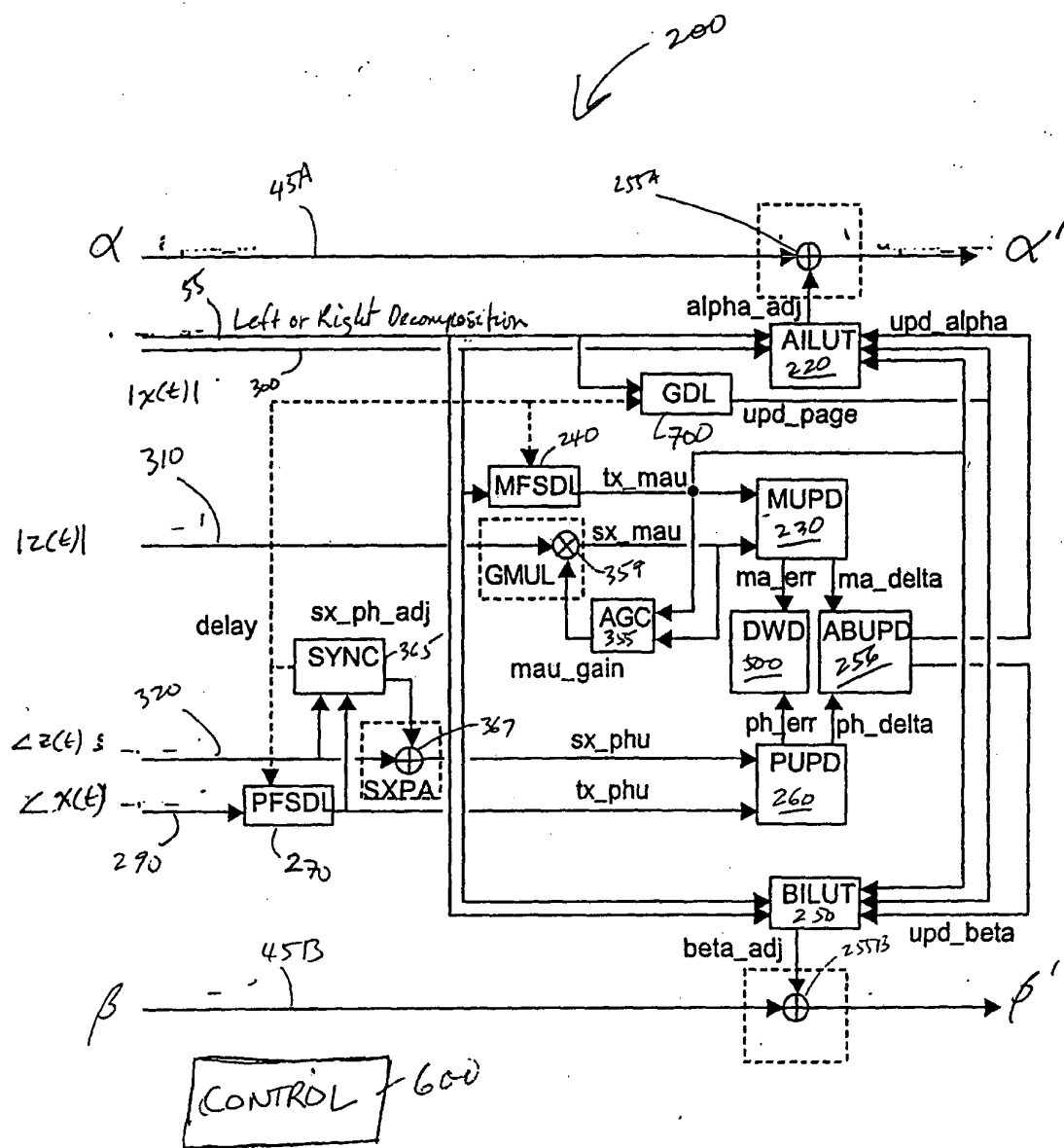


FIG 5

To predistortion subsystem

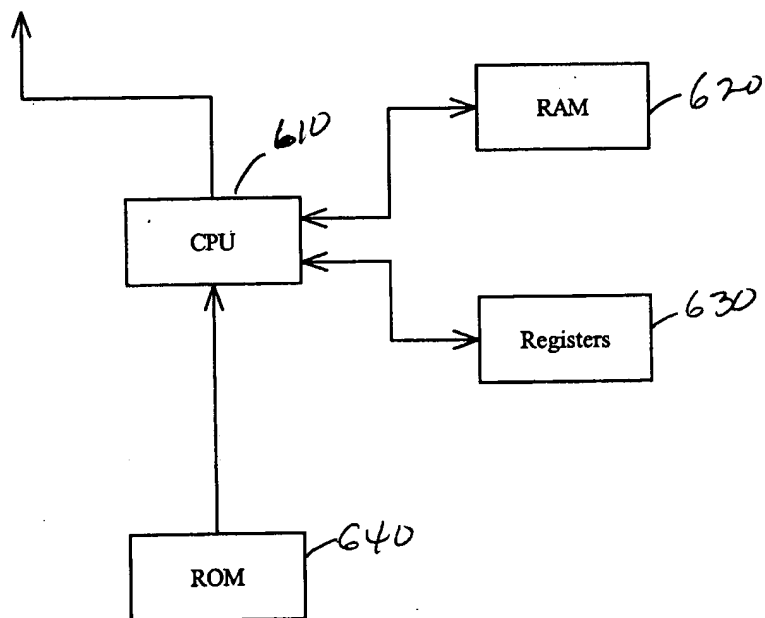


FIG 5B

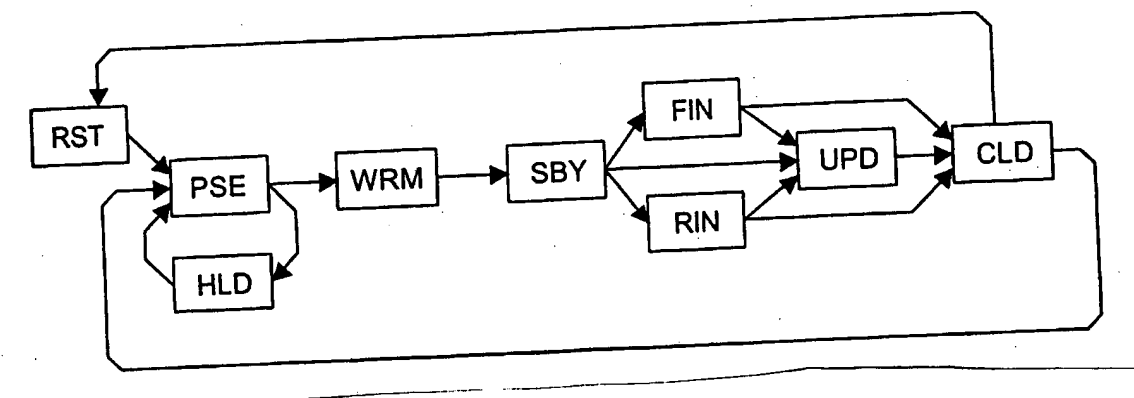


FIG 5A

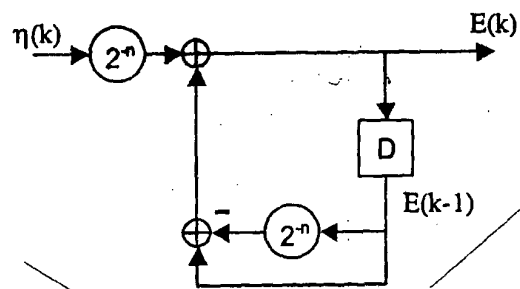


FIG 5C

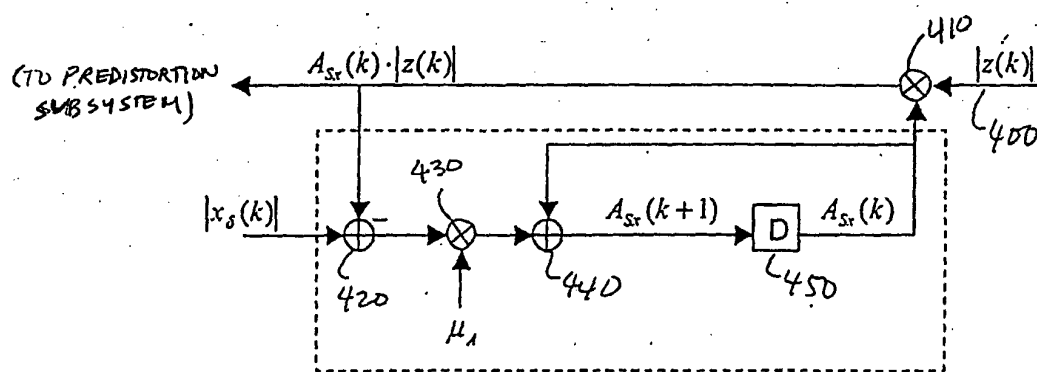


FIG 6A

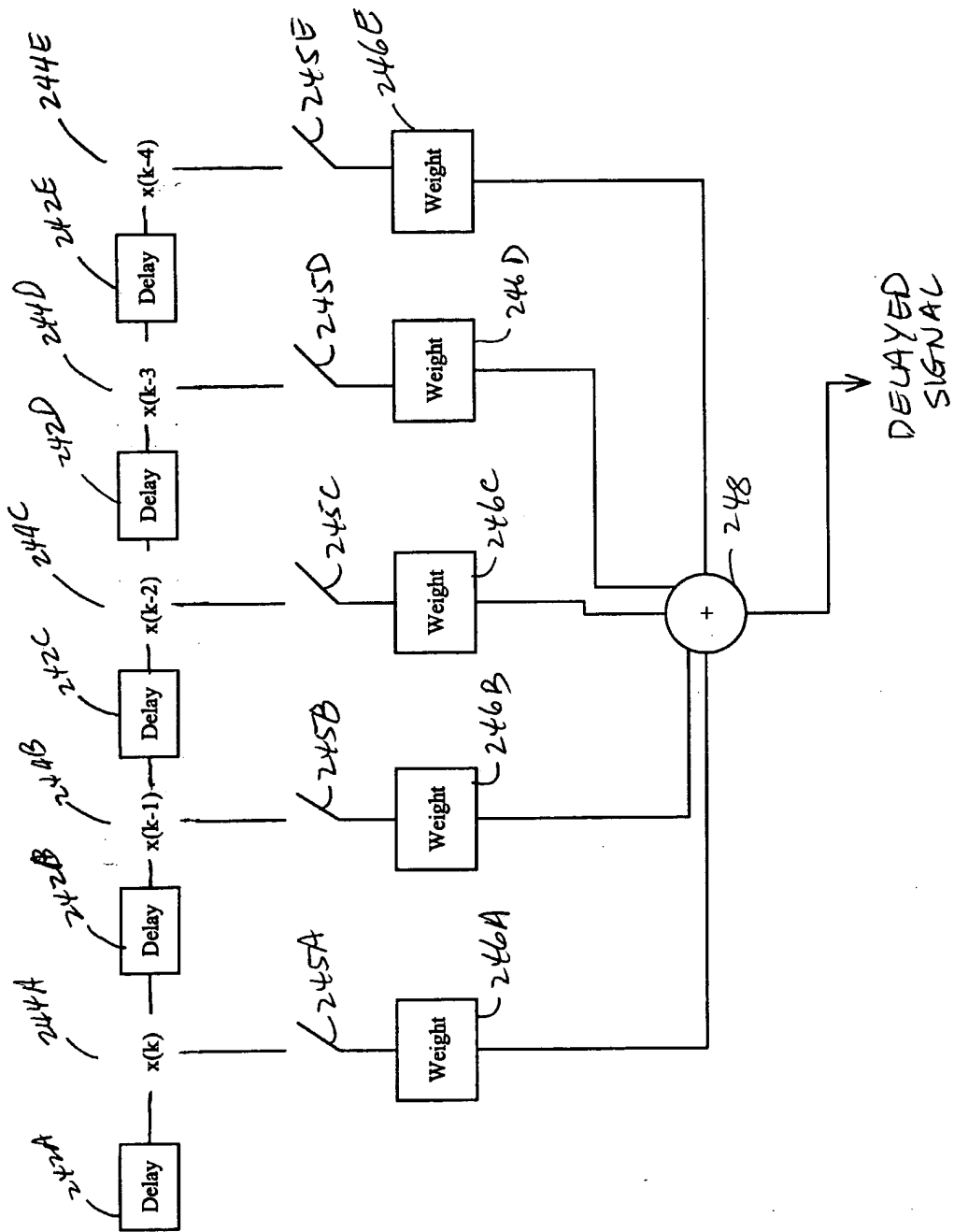


FIG 6